

2700

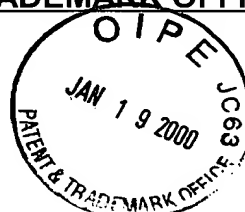
276

2721

PATENT  
2207/7533

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS : SHEAFFER, Gad. S.  
SERIAL NO. : 09/466,964  
FILED : December 20, 1999  
FOR : BUFFER PRE-LOADING FOR MEMORY SERVICE  
INTERRUPTIONS  
GROUP ART UNIT : Not assigned  
EXAMINER : Not assigned



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2700 MAIL ROOM

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K. Ward  
5/4/00  
#2/Re Amended  
A. Ent

ASSISTANT COMMISSIONER OF PATENTS  
Washington, DC 20231

**PRELIMINARY AMENDMENT**

S I R:

Preliminary to examination of the above-identified application, please amend the application as follows:

**In the Claims:**

Please add the following new claims 23-30:

23. (New) An apparatus comprising:  
a memory buffer; and  
a memory controller coupled to said memory buffer, said memory controller to operate in a first mode maintaining a first level of buffering in said memory buffer and to switch to a second mode maintaining a second level of buffering that is higher than the first level of buffering in response to an advance indication of a memory service interruption.

AI Subcl  
23

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